

NESO SERIES

Embedded Computer System



NESO SERIES · Hardware Revision History



Zuverlässige
Qualität
Made in Germany



Title: Hardware Revision History		Revision 01
Project NESO	Author Stefan Schrader	

Revision process			
Revision	Editor	Date	Modifications
01	Schrader	08.01.2010	First issue



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Content

1	Hardware revisions	3
2	Revision history	4
2.1	Hardware changes made from V1.0 to V1.1	4
2.2	Hardware changes made from V1.1 to V1.2	4
2.2.1	CPU-RTC	4
2.2.2	Display connector	4
2.2.3	MDB / RS485	4
2.2.4	USB-OTG	4
2.2.5	Microphone BIAS	4
2.2.6	SPI / keypad interface	4
2.3	Hardware changes made from V1.2 to V1.3	4
2.3.1	EMI	4
2.4	Hardware changes made from V1.3 to V1.31	4
2.4.1	RS485	4

1 Hardware revisions

Hardware Revision	Marking on PCB
V1.0	0340 NESO V1.0
V1.1	0340 NESO V1.1
V1.2	0340 NESO V1.2
V1.3	0340 NESO V1.3
V1.31	0340 NESO V1.3.1

2 Revision history

2.1 Hardware changes made from V1.0 to V1.1

V1.1 is the first version available for sales. Several minor changes have been made since V1.0.

2.2 Hardware changes made from V1.1 to V1.2

2.2.1 CPU-RTC

Changed routing of real time clock quartz signals to avoid distortions caused by USB-OTG signals.

2.2.2 Display connector

Internal connector for 7" display slightly moved for better accessibility

2.2.3 MDB / RS485

MDB interface is replaced by RS485 full Duplex interface.

2.2.4 USB-OTG

Capacitance at VBUS corrected to requirement by USB specification.

2.2.5 Microphone BIAS

BIAS Voltage on internal audio connector now able to drive electrets microphone

2.2.6 SPI / keypad interface

SPI-interface multiplexed with keypad-interface, configurable by software.

2.3 Hardware changes made from V1.2 to V1.3

2.3.1 EMI

Routing optimized to reduce EMI.

2.4 Hardware changes made from V1.3 to V1.31

2.4.1 RS485

Transceiver is no longer active while system is in reset.