

MC4558

Wide bandwidth dual bipolar operational amplifier

Datasheet – production data

Features

- Internally compensated
- Short-circuit protection
- Gain and phase match between amplifier
- Low power consumption
- Pin-to-pin compatible with MC1458/LM358
- Gain bandwidth (at 100 kHz): 5.5 MHz

Description

The MC4558 is a high performance monolithic dual operational amplifier.

The circuit combines all of the outstanding features of the MC1458, and in addition possesses three times the unity gain bandwidth of the industry standard.

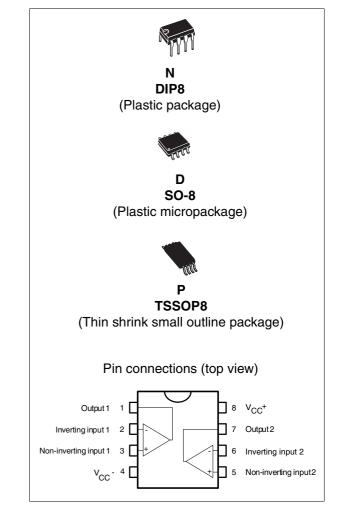


Table 1. Device summary

Order codes	Temperature range	Package	Packing	Marking
MC4558CN		DIP8	Tube	MC4558CN
MC4558CD/CDT	0 °C to +70 °C	SO-8	Tube or tape & reel	4558C
MC4558CPT		TSSOP8	Tape & reel	45560
MC4558ID/IDT	-40 °C to +105 °C	SO-8	Tube or tape & reel	45581

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This is information on a product in full production.

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1 Absolute maximum ratings

Symbol	Parameter MC4558I MC4558C		MC4558C	Unit		
V _{CC}	Supply voltage	±ź	V			
V _i ⁽¹⁾	Input voltage ±15					
V _{id} ⁽²⁾	Differential input voltage	±ć	30	V		
P _{tot}	Power dissipation	68	mW			
	Output short-circuit duration	short-circuit duration Infinite				
T _{oper}	Operating free-air temperature range	-40 to +105 0 to +70		°C		
R _{thja}	Thermal resistance junction-to-ambient: SO-8 TSSOP8 DIP8	12 12 8	°C/W			
	HBM: Human body model ⁽³⁾	50	00			
ESD	MM: Machine model ⁽⁴⁾	20	200			
	CDM: Charged device model	15	00			

 Table 2.
 Key parameters and their absolute maximum ratings

1. Input voltage is with respect to the midpoint between Vcc+ and Vcc-. Its value must never exceed 15 V or the magnitude of Vcc, whichever is less.

2. Differential voltages are the non-inverting input terminal with respect to the inverting input terminal.

3. Human body model, 100 pF discharged through a 1.5 $k\Omega$ resistor into pin of device.

 Machine model ESD, a 200 pF cap is charged to the specified voltage, then discharged directly into the IC with no external series resistor (internal resistor < 5 Ω), into pin of device.

Table 3.Operating conditions

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	Supply voltage	±2	±20	V



2 Typical application schematic

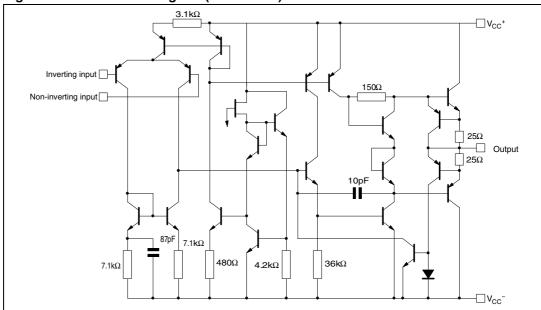


Figure 1. Schematic diagram (1/2MC4558)



3 Electrical characteristics

Table 4.Electrical characteristics for $V_{CC} = \pm 15 \text{ V}$, $T_{amb} = 25 \text{ °C}$ (unless otherwise specified)					
Symbol	Parameter	Min.	Тур.	Max.	Unit
V _{io}	Input offset voltage ($R_s \le 10k\Omega$) $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max.}$		1	5 6	mV
I _{io}	Input offset current $T_{amb} = +25^{\circ}C$ $T_{min.} \leq T_{amb} \leq T_{max.}$		20	100 200	nA
l _{ib}	Input bias current $T_{amb} = +25^{\circ}C$ $T_{min.} \leq T_{amb} \leq T_{max.}$		50	400 500	nA
A _{vd}	Large signal voltage gain ($R_L = 2k\Omega V_0 = \pm 10V$) $T_{amb} = +25^{\circ}C$ $T_{min.} \leq T_{amb} \leq T_{max.}$	50 25	200		V/mV
SVR	Supply voltage rejection ratio ($R_s \le 10 k\Omega$) $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max.}$	77 77	90		dB
I _{CC}	Supply current, all amplifiers, no load T _{amb} = +25°C T _{min} . ≤T _{amb} ≤T _{max.}		2.3	4.5 6	mA
V _{icm}	Input common mode voltage range $T_{amb} = +25^{\circ}C$ $T_{min.} \leq T_{amb} \leq T_{max.}$	±12 ±12			v
CMR	Common-mode rejection ratio ($R_s \le 10 k\Omega$) $T_{amb} = +25^{\circ}C$ $T_{min.} \le T_{amb} \le T_{max.}$	70 70	90		dB
I _{os}	Output short-circuit current	10	20	40	mA
Vo	$ \begin{array}{l} \text{Output voltage swing} \\ T_{amb} = +25^\circ \text{C} \ \text{R}_L = 10 \text{k}\Omega \\ \text{R}_L = 2 \text{k}\Omega \\ T_{min} \cdot \leq T_{amb} \leq T_{max}. \ \text{R}_L = 10 \text{k}\Omega \\ \text{R}_L = 2 \text{k}\Omega \end{array} $	±12 ±10 ±12 ±10	±14 ±13		v
SR	Slew rate $V_i = \pm 10$, $R_L = 2k\Omega$, $C_L = 100$ pF, $T_{amb} = 25^{\circ}$ C, unity gain	1.5	2.2		V/µs
t _r	Rise time $V_i = \pm 20$ mV, $R_L = 2k\Omega$, $C_L = 100$ pF, $T_{amb} = 25$ °C, unity gain		0.3		μs
K _{OV}	Overshoot $V_i = \pm 20$ mV, $R_L = 2k\Omega$, $C_L = 100$ pF, $T_{amb} = 25$ °C, unity gain		15		%
R _i	Input resistance	0.3	2		MΩ
C _i	Input capacitance		1.4		pF
R _o	Output resistance		75		Ω
В	Unity gain bandwidth		2.8		MHz

Table 4. Electrical characteristics for $V_{CC} = \pm 15 \text{ V}$, $T_{amb} = 25 \text{ °C}$ (unless otherwise specified)



		r	r	•	
Symbol	Parameter	Min.	Тур.	Max.	Unit
GBP	Gain bandwidth product $V_i = 10mV$, $R_L = 2k\Omega$, $C_L = 100pF$, f = 100kHz, $T_{amb} = 25^{\circ}C$		5.5		MHz
THD	Total harmonic distortion f = 1kHz, $A_v = 20$ dB, $R_L = 2k\Omega$, $V_o = 2V_{pp}$, $C_L = 100$ pF, $T_{amb} = 25^{\circ}$ C		0.008		%
e _n	Equivalent input noise voltage ($R_S = 100\Omega$, f = 1kHz)		12		<u>nV</u> √Hz
V ₀₁ /V ₀₂	Channel separation		120		dB

Table 4.Electrical characteristics for $V_{CC} = \pm 15 \text{ V}$, $T_{amb} = 25 \text{ °C}$ (unless otherwise specified)



Figure 2. Transient response test circuit

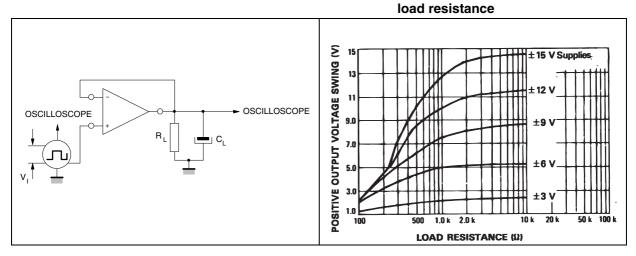
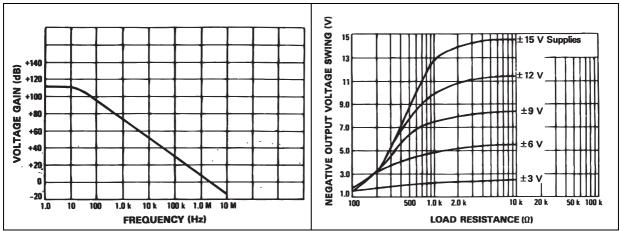


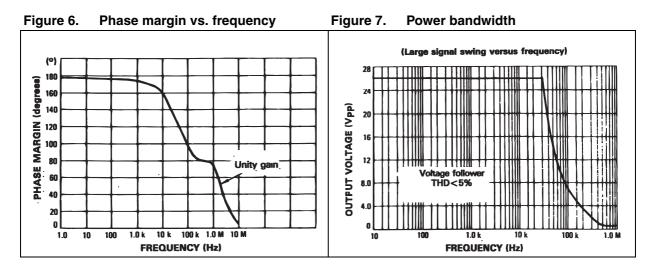
Figure 3.

Figure 4. Open loop frequency response

Figure 5. Negative output voltage swing vs. load resistance

Positive output voltage swing vs.





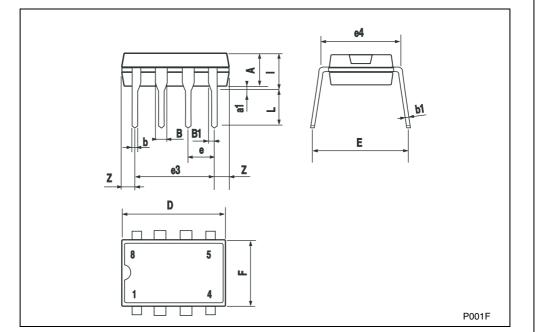
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4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

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DIM.		mm.			inch	
DIW.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А		3.3			0.130	
a1	0.7			0.028		
В	1.39		1.65	0.055		0.065
B1	0.91		1.04	0.036		0.041
b		0.5			0.020	
b1	0.38		0.5	0.015		0.020
D			9.8			0.386
Е		8.8			0.346	
е		2.54			0.100	
e3		7.62			0.300	
e4		7.62			0.300	
F			7.1			0.280
I			4.8			0.189
L		3.3			0.130	

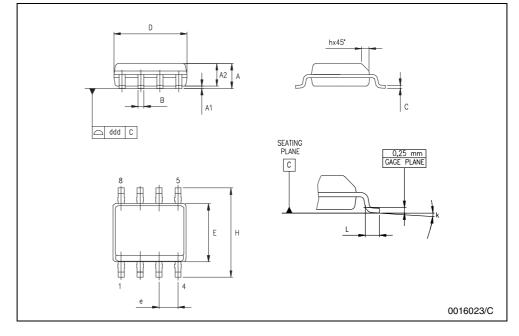
Figure 8. DIP8 package





DIM.		mm.			inch	
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX
А	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.04		0.010
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.010
D	4.80		5.00	0.189		0.197
E	3.80		4.00	0.150		0.157
е		1.27			0.050	
н	5.80		6.20	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
k			8° (max.)	•	
ddd			0.1			0.04

Figure 9. SO-8 package



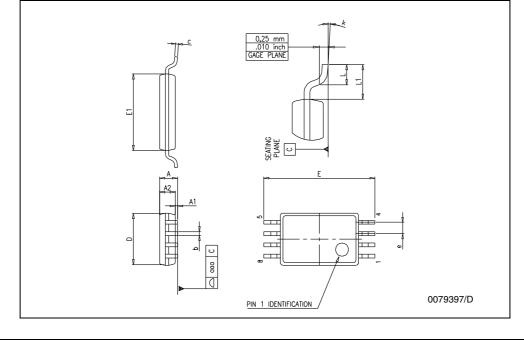






	TSSOP8 MECHANICAL DATA						
DIM		mm.			inch		
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX	
А			1.2			0.047	
A1	0.05		0.15	0.002		0.006	
A2	0.80	1.00	1.05	0.031	0.039	0.041	
b	0.19		0.30	0.007		0.012	
С	0.09		0.20	0.004		0.008	
D	2.90	3.00	3.10	0.114	0.118	0.122	
E	6.20	6.40	6.60	0.244	0.252	0.260	
E1	4.30	4.40	4.50	0.169	0.173	0.177	
е		0.65			0.0256		
к	0°		8°	0°		8°	
L	0.45	0.60	0.75	0.018	0.024	0.030	
L1		1			0.039		

Figure 10. TSSOP8 package





5 Revision history

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Date	Revision	Changes
Oct-2001	1	Initial release.
Oct-2005	2	 The following changes were made in this revision: <i>Table 3.: Operating conditions on page 2</i> updated with Vcc min. and max. Addition of supplementary data in <i>Table 2.: Key parameters and their absolute maximum ratings on page 2</i> Minor grammatical and formatting changes throughout.
13-Apr-2012	3	 ESD MM changed from 500 V to 200 V in <i>Table 2: Key parameters</i> and their absolute maximum ratings Order codes MC4558IN and MC4558IPT removed from <i>Table 1.:</i> <i>Device summary</i> Minor text and formatting changes throughout.



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